LVDS Receiver MODULE

3DLV3208VS1373

3V Eight Line Receivers, based on Quad

Pin Assignment (Top View)

SOP 34 (Pitch : 0.65 mm)

1 Rin1- 18 Rin7-
2 Rin1+ 19 Rin7+
3 Rout1 20 Rout7
4 EN1 21 #EN2
5 Rout2 22 Rout8
6 Rin2+ 23 Rin8+
7 Rin2- 24 Rin8-
8 GND 25 Vdd
9 GND 26 Vdd
10 Rin5- 27 Rin3-
11 Rin5+ 28 Rin3+
12 Rout5 29 Rout3
13 EN2 30 #EN1
14 Rout6 31 Rout4
15 Rin6+ 32 Rin4+
16 Rin6- 33 Rin4-
17 GND 34 Vdd

Features

- >400 Mbps (200 MHz) switching rates
- ±450 mV differential signalling
- 3.3 V power supply
- Ultra low power dissipation
- 0.2 ns differential skew (typical)
- 6 ns maximum propagation delay
- Conforms to ANSI/TIA/EIA-644 LVDS standard
- Integrated 110-Ω Line Termination Resistors
- Cold sparing all I/O pins
- Variable Temperature range
- 0°C to 70°C
- -40°C to +85°C
- -55°C to +125°C
- Available screening option for high reliability application

General description

The 3DLV3208VS1373 is Eight CMOS differential line receiver designed for applications requiring ultra low power dissipation and high data rates. The device is designed to support data rates in excess of 400 Mbps (200 MHz) utilizing Low Voltage Differential Signaling (LVDS) technology.

The 3DLV3208VS1373 accepts low voltage (350 mV typical) differential input signals and translates them to 3V CMOS output levels.

The 3DLV3208VS1373 and companion line driver (3DLV3108VS1372) provide a new alternative to high power pseudo-ECL devices for high speed point-to-point interface applications.

LVDS Module

3D Plus SA reserves the right to cancel product or specifications without notice

3DFP-0373-REV 4- DEC.2013
LVDS Receiver MODULE
8 Lines-SOP

Low-voltage differential signaling Receiver

MODULE

3DLV3208VS1373

3V Eight Line Receivers, based on Quad

Mechanical Drawing

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>VDD</td>
<td>3.0</td>
<td>3.6</td>
<td>V</td>
</tr>
<tr>
<td>Input High Voltage</td>
<td>VIH</td>
<td>2.0</td>
<td>VDD</td>
<td></td>
</tr>
<tr>
<td>Input Low Voltage</td>
<td>VIL</td>
<td>GND</td>
<td>0.8</td>
<td>V</td>
</tr>
</tbody>
</table>

Test Tools

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>VDD</td>
<td>-0.5 to 4.0</td>
<td>V</td>
</tr>
<tr>
<td>Input Voltage (Dim)</td>
<td>Vin</td>
<td>-0.5 to VDD +0.5</td>
<td>V</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>Tstg</td>
<td>-65 to 150</td>
<td>°C</td>
</tr>
</tbody>
</table>

DC OPERATING CONDITIONS

ABSOLUTE MAXIMUM RATINGS

DC Characteristics

Module Marking

3DLV3208VS1373

Note:
Permanent device damage may occur if "ABSOLUTE MAXIMUM RATINGS" are exceeded.
Functional operation should be restricted to recommended operating condition.
Exposure to higher than recommended voltage for extended periods of time could affect device reliability.

Quality Level:
N = Commercial Grade
B = Industrial Grade
S = Space Grade
C = Custom

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